

micro T-Kernel Implementation Specification M16C (M16C62P)

Real-time Operating System for M16C Family



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Preface

This document gives the specification for implementing micro T-Kernel on a specific target board.

The applicable board is Renesas Starter Kit for M16C/62P. Please refer to this website for more information on Renesas Starter Kit: <http://www.renesas.com/rsk>.

The applicable micro T-Kernel version is 1.01.00.

The specifications given in this document correspond to the hardware-dependent implementation-specific parts of the micro T-Kernel specification.

Please refer to the micro T-Kernel specification in regard to the specification of micro T-Kernel.

In addition, refer to the relevant specifications as for the specifications of hardware such as board and CPU.

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1 CPU

1.1 Hardware Specifications

CPU: M16C/62P (M30626FJPGP)
Renesas Technology Corp.
ROM: 512 KB (On-chip FlashROM) and additional 4 KB On-chip Data Flash
RAM: 31 KB (On-chip SRAM)

1.2 Protection level and operation modes

The processor runs at Single Chip Mode (PM0.PM00=PM0.PM01=0) on this platform.

Only the internal areas (SFR, internal RAM, and internal ROM) are accessible. The ports (P0-P10) can be used either as programmable I/O ports or as I/O ports for internal peripheral functions.

Since this system operates in Single Chip Mode, there is no switch-over of the protection level. Even if any of the protection level is specified, it shall be treated as protection level 0.

1.3 Endian

M16C family microcomputer uses little-endian format for both bit and byte.

2 Memory

2.1 Overall Memory Map

Memory map of overall system is shown below.

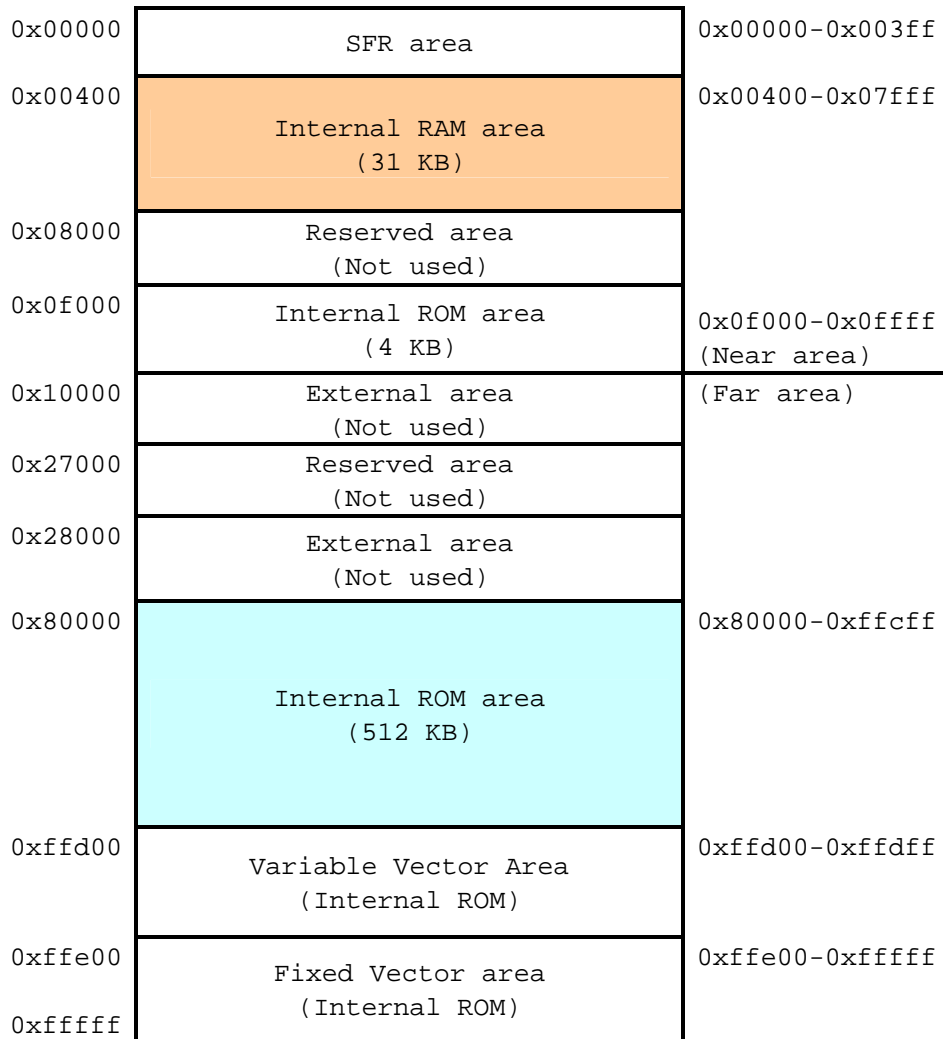


Figure 2-1 Overall Memory Map

The INTB register value is initialized to the start address of Variable Vector Area, namely, 0xffd00.

2.2 Internal ROM Memory Map

There are two internal ROM areas in M16C/62P: Near ROM area and Far ROM area.

Near ROM area is a 4 KB (0x0f000-0x0ffff) area.

Far ROM area is a 512 KB (0x80000-0xfffff) area.

Interrupt vector table and micro T-Kernel code shall be located in the Far ROM area.

Memory map of Near ROM area is shown below.

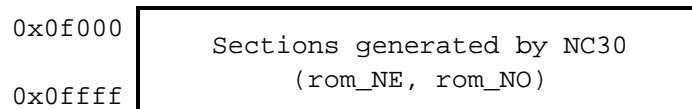


Figure 2-2 Memory Map of Near ROM area

Memory map of Far ROM area is shown below.

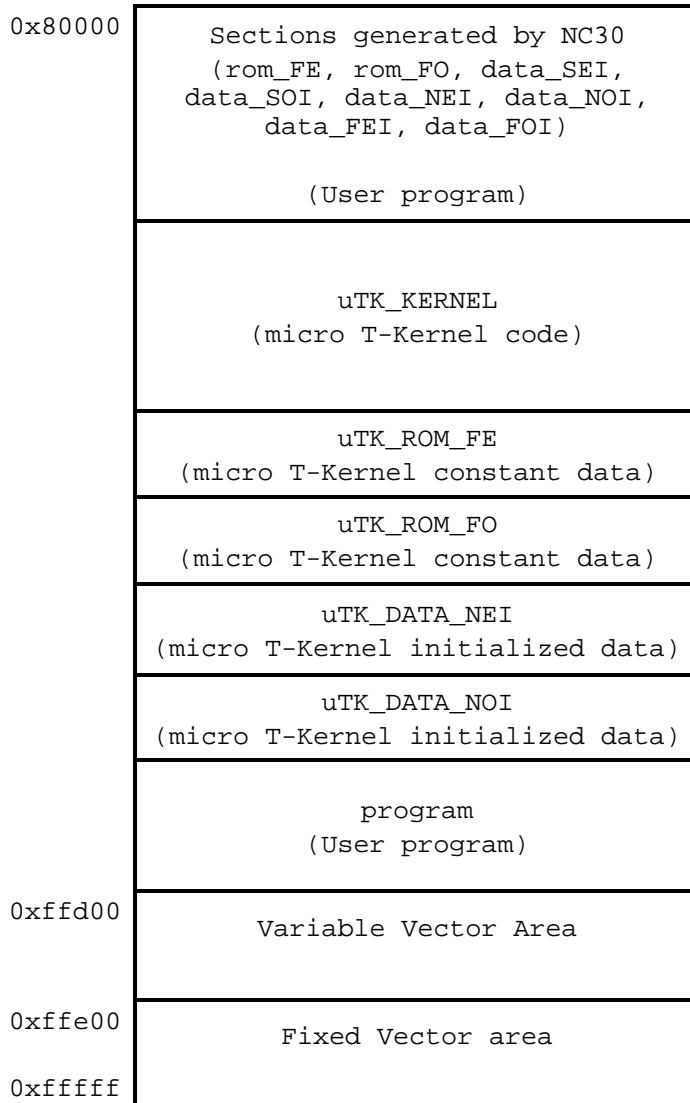


Figure 2-3 Memory Map of Far ROM area

The following explains the additional sections defined here:

- uTK_KERNEL
This section stores the micro T-Kernel kernel code.
- uTK_ROM_FE and uTK_ROM_FO
These two sections store the constant data used by the micro T-Kernel.
- uTK_DATA_NEI and uTK_DATA_NOI
These two sections store the initialized data used by the micro T-Kernel.
- program
This section stores the user programs (not used by the micro T-Kernel).

Memory map for fixed vector area in Internal ROM area is shown below.

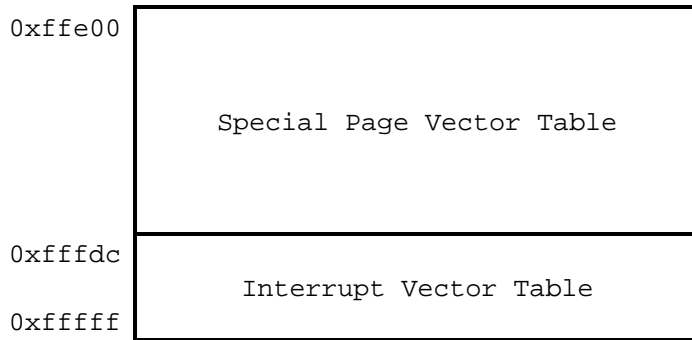


Figure 2-4 Memory Map of fixed vector area

2.3 Internal RAM Memory Map

In internal RAM, an area of 31 KB (0x00400-0x07ffff) is implemented. The memory map of internal RAM area is shown below.

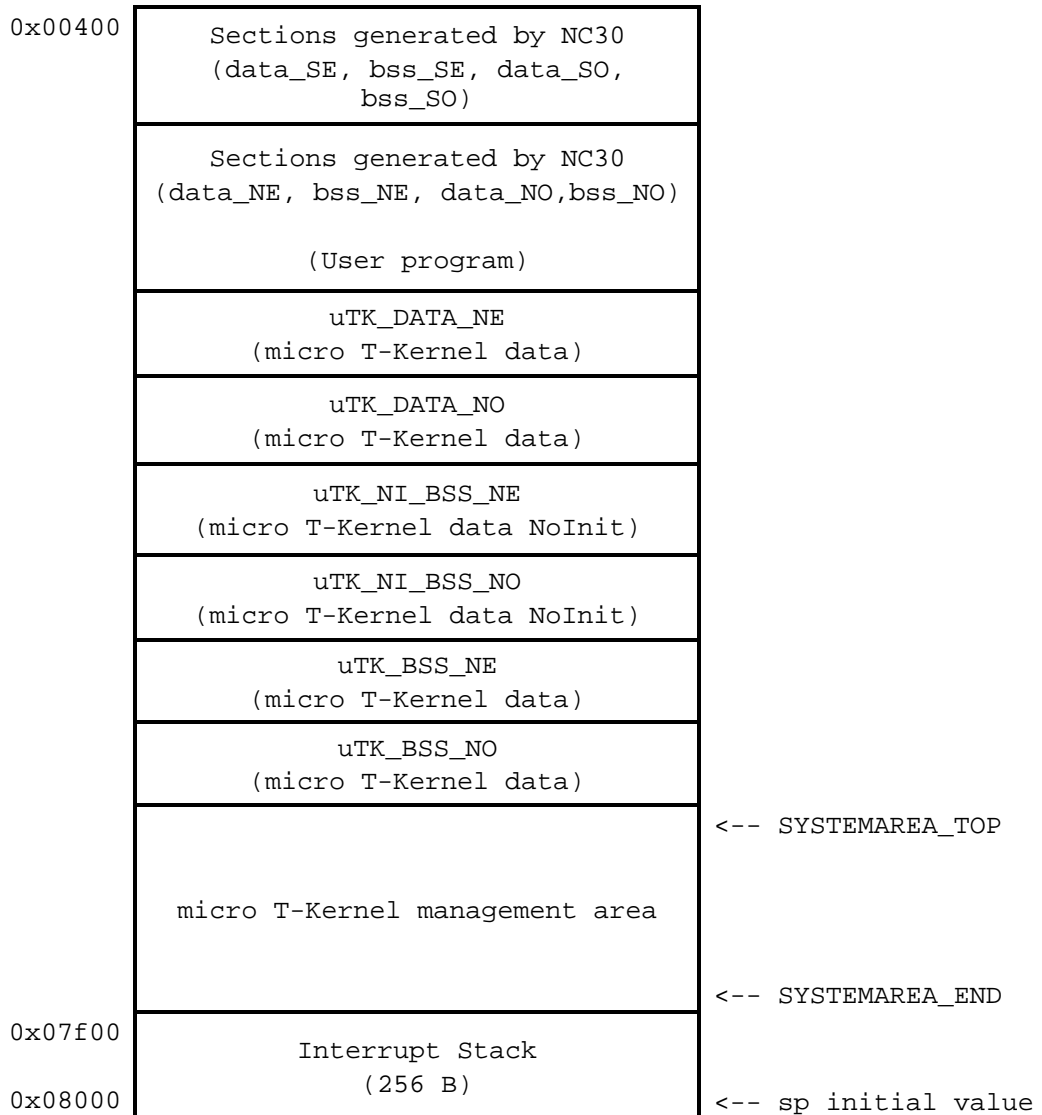


Figure 2-5 Memory Map of internal RAM area

The data section and BSS section are separate for micro T-Kernel program and user program. They are located in ascending order from the lower byte of the on-chip RAM.

The following explains the additional sections defined for micro T-Kernel:

- `uTK_DATA_NE` and `uTK_DATA_NO`
These sections are where the micro T-Kernel initialized data is stored.
- `uTK_BSS_NE` and `uTK_BSS_NO`
These sections are where the micro T-Kernel uninitialized data is stored. The data in this section must be cleared during kernel initialization.
- `uTK_NI_BSS_NE` and `uTK_NI_BSS_NO`
These sections are where the micro T-Kernel uninitialized data is stored. The data in this section will not be cleared during kernel initialization when "USE_NOINIT" macro is set to 1.

The micro T-Kernel management area is memory space dynamically managed by the micro T-Kernel memory management function.

Normally, all unused memory spaces are allocated to the micro T-Kernel management area, but this can be changed in system configuration. micro T-Kernel management area is allocated to the designated area between "SYSTEMAREA_TOP" (or the end of `uTK_BSS` section) and "SYSTEMAREA_END" in configuration file.

2.4 Stack

There are three kinds of stacks in micro T-Kernel as below.

(1) System stack

This stack is used in non-interrupt handler, and one system stack exists per each task.

Since there is no concept of protection level in micro T-Kernel, unlike T-Kernel, user stack and system stack are not separated.

The stack is usually used during kernel system call processing.

If `TA_USERBUF` is specified, the system stack for a task should be allocated in the memory area explicitly during task creation. Otherwise, the system stack is allocated in micro T-Kernel management area.

The stack area for the first kernel task "inittsk" is allocated in micro T-Kernel management area. Its default size is 1024 bytes.

(2) Interrupt stack

This stack is used in an interrupt handler, and the stack area independent of system stack shall be allocated to it.

Since an interrupt stack is commonly used in system, task switching shall not happen during use.

When an interrupt occurred, micro T-Kernel switches system stack to the interrupt stack.

There is only one interrupt stack in the entire system.

(3) Temporary stack

This stack is for micro T-Kernel internal use only. It is used when 'dispatch_to_schedtsk()' is called. Its usage depends on the stack usage of `kn1_low_pow()` and task dispatch hook stop routine (if `USE_HOOK_TRACE` macro is enabled).

This temporary stack is allocated in the "uTK_NI_BSS" section.

3 Interrupts and Exceptions

The M16C/62P group provides Fixed Vector Table, which is allocated on a fixed address, and Variable Vector Table, which may be allocated on an arbitrarily address.

3.1 Interrupt Vector Table in Fixed Vector Table

0xffffdc	Undefined instruction
0xffffe0	Overflow
0xffffe4	BRK instruction
0xffffe8	Address match
0xffffec	Single step
0xfffff0	Watchdog timer
0xfffff4	DBC
0xfffff8	NMI
0xfffffc	Reset handler
0xfffff	

Figure 3-1 Interrupt Vector Table in Fixed Vector Table

Reset vector address must be hard-coded in micro T-Kernel. After reset, the CPU starts executing the program from this address.

3.2 Interrupts definition number

The immediate values (0-63) of interrupt number shall be used by `dintno`, the interrupt definition numbers defined with `tk_def_int()`.

The following is the Interrupt Exception Handling Vector Table.

Vector Address	Interrupt Number	Interrupt Sources
0xffd00	0	BRK Instruction
0xffd04	1	(Reserved)
0xffd08	2	(Reserved)
0xffd0c	3	(Reserved)
0xffd10	4	INT3
0xffd14	5	Timer B5
0xffd18	6	Timer B4, UART1 Bus Collision Detect
0xffd1c	7	Timer B3, UART0 Bus Collision Detect
0xffd20	8	SI/O4, INT5
0xffd24	9	SI/O3, INT4
0xffd28	10	UART 2 Bus Collision Detection
0xffd2c	11	DMA0

0xffd30	12	DMA1
0xffd34	13	Key Input Interrupt
0xffd38	14	A/D
0xffd3c	15	UART2 Transmit, NACK2
0xffd40	16	UART2 Receive, ACK2
0xffd44	17	UART0 Transmit, NACK0
0xffd48	18	UART0 Receive, ACK0
0xffd4c	19	UART1 Transmit, NACK1
0xffd50	20	UART1 Receive, ACK1
0xffd54	21	Timer A0 [micro T-Kernel use]
0xffd58	22	Timer A1
0xffd5c	23	Timer A2
0xffd60	24	Timer A3
0xffd64	25	Timer A4
0xffd68	26	Timer B0
0xffd6c	27	Timer B1
0xffd70	28	Timer B2
0xffd74	29	INT0
0xffd78	30	INT1
0xffd7c	31	INT2
0xffd80	32	INT #32 (TRAP_SVC)
0xffd84	33	INT #33
0xffd88	34	INT #34 (TRAP_RETINT)
0xffd8c	35	INT #34 (TRAP_RETINT)
0xffd90	36	INT #36
0xffd94	37	INT #37
0xffd98	38	INT #38
0xffd9c	39	INT #39 (TRAP_DEBUG)
0xffda0	40	INT #40
0xffda4	41	INT #41
0xffda8	42	INT #42
0xffdac	43	INT #43
0xffdb0	44	INT #44
0xffdb4	45	INT #45
0xffdb8	46	INT #46
0xffdbc	47	INT #47
0xffdc0	48	INT #48
0xffdc4	49	INT #49
0xffdc8	50	INT #50
0xffdcc	51	INT #51
0xffdd0	52	INT #52
0xffdd4	53	INT #53
0xffdd8	54	INT #54
0xffddc	55	INT #55
0xffde0	56	INT #56
0xffde4	57	INT #57
0xffde8	58	INT #58
0xffdec	59	INT #59
0xffdf0	60	INT #60
0xffdf4	61	INT #61
0xffdf8	62	INT #62
0xffdfc	63	INT #63

Table 3-1 Interrupt Vector Table in Variable Vector Table

3.3 INT exception assignments

The INT exceptions are assigned to interrupt definition numbers #32 (0x20) through #40 (0x28). Each INT exception is used as shown below:

```
INT #32 micro T-Kernel system call/extended SVC
INT #33 (Reserved)
INT #34 "tk_ret_int()" system call
INT #35 Task dispatcher call
INT #36 (Reserved)
INT #37 (Reserved)
INT #38 (Reserved)
INT #39 Debugger support function
INT #40 (Reserved)
```

INT #41 to #63 are available for users.

3.4 Interrupt handler

If an interrupt handler is defined in the "vector.c" file, the address of the handler shall be defined to the corresponding interrupt number. The interrupt number defined in "vector.c" file can be used with tk_def_int().

If an interrupt occurs, it directly jumps to the set address of interrupt handler. Therefore, the context shall be saved in the processing of the interrupt handler.

An entry routine is defined by using INT_ENTRY macro. When "INT_ENTRY vecno" is written in assembly code, the entry routine of the interrupt handler named "knl_inthdr_entryN" (N is an interrupt number) is generated. And if "knl_inthdr_entryN" is defined in "vector.c" file, the interrupt number can be used.

The example of Exception/Interrupt handling routine code is shown as below.

```
-----
[include\tk\sysdepend\app_m16c62p\asm_depend.h]

/*
 * Exception/Interrupt entry common processing
 * system IPL is set to KNL_INTMASK
 */
#pragma ASM
INT_ENTRY .macro vecno
    .glb knl_inthdr_entry@vecno
    .glb _knl_intvec
knl_inthdr_entry@vecno:
    pushm a0,a1                ; save A0, A1

    mov.w #vecno, a0
    mov.w #_knl_intvec, a1
    add.w #(vecno << 2), a1
    jmp.i.a    [a1]

.endm
#pragma ENDASM
-----
```

The entry routine executes the following processing

- Save A0 and A1 to the stack
- Save an interrupt number in A1
- Jump to the handler set in `tk_def_int()`

Since `tk_def_int()` is premised on this processing, A0 and A1 need to be saved in stack if handler is defined without using `INT_ENTRY` macro.

The PC and FLG are automatically saved to the stack by the CPU. The A0 and A1 are used as the working register for branch processing to each handler, and thus, saved by each handling routine.

Registers other than A0 and A1 are not saved at "INT_ENTRY" and therefore needs to be saved at each handler as needed. For high-level language support routine defined by `tk_def_int()`, those registers are saved by "knl_inthdr_startup" routine during interrupt startup. If a high-level language support routine is not used, those registers need to be saved explicitly.

In order to realize the delayed dispatching with `tk_ret_int()`, interrupt nesting count ("knl_int_nest") is incremented within a high-level language support routine. If a high-level language support routine is not used, "knl_int_nest" needs to be incremented explicitly.

If 1 is set to "USE_FULL_VECTOR" of "uk_config_depend.h", "INT_ENTRY" macro is automatically used to all vectors and all interrupts can be treated with `tk_def_int()`. By default setting, 1 shall be set to "USE_FULL_VECTOR".

3.5 Kernel Interrupt Mask Level

Kernel interrupt mask level (`KNL_INTMASK`) is the system IPL (`FLG.IPL`) set during kernel internal processing (e.g. system call critical sections).

This parameter is configurable in the following kernel header files. One is used by C code; the other is used by assembly code.

```
-----
[include\tk\sysdepend\app_m16c62p\sysdef_depend.h]

/*
 * Kernel interrupt mask level (System IPL)
 */
#define KNL_INTMASK      4          /* 0-7 */
#ifdef NC30
#pragma ASM
_KNL_INTMASK      .define      40h  /* 00h, 10h, 20h, ..., 70h */
#pragma ENDASM
#endif
-----

[include\tk\sysdepend\app_m16c62p\sysdef_depend.inc]

;/*
; * Kernel interrupt mask level (System IPL)
; */
KNL_INTMASK .equ      40h  ;/* 00h, 10h, 20h, ..., 70h */
-----
```

User can change the kernel interrupt mask level according to the application requirement. Therefore, for the interrupt which requires high-speed processing, if its IPL is higher than `KNL_INTMASK`, its interrupt handler will be processed as soon as possible regardless of the internal state of micro T-Kernel.

For normal operation, the IPL of kernel dependent interrupt, such as timer interrupt, must be lower or equal to `KNL_INTMASK`. Timer IPL is defined the following kernel header file:

```
-----
[kernel\sysdepend\device\app_m16c62p\tkdev_conf.h]

/*
 * Timer interrupt level
 */
#define TIMER_INTLEVEL          3
-----
```

The system call of micro T-Kernel cannot be executed from the interrupt handler, whose IPL is higher than the kernel interrupt mask level (`KNL_INTMASK`), because it may cause system crash when a system call is issued. The error code "E_CTX" returns if the system call is issued.

The system call of micro T-Kernel can be issued from the interrupt handler, whose IPL is lower or equal to the kernel interrupt mask level (`KNL_INTMASK`).

4 Initialization and Startup Processing

4.1 micro T-Kernel Startup Procedure

When system is reset, micro T-Kernel starts up.

The procedures from the startup of micro T-Kernel to the call of main function are as follows.

```
[kernel\sysdepend\device\app_m16c62p\icrt.c]
```

- (1) Set interrupt stack pointer (ISP)
- (2) Initialize processor mode
- (3) Initialize system clock
- (4) Initialize FLG register
- (5) Initialize FB register
- (6) Initialize SB register
- (7) Set variable vector address in INTB register
- (8) Set the initial value of DATA section (copy from ROM to RAM)
- (9) Clear BSS section to 0
- (10) Calculate the range of micro T-Kernel management area
- (11) Reset interrupt stack pointer (ISP)
- (12) Set FLG register with system IPL = KNL_INTMASK
- (13) Set FB register to 0
- (14) Call "main" function (sysinit_main.c)

4.2 User initialization program

A user initialization program is the routine to initialize/terminate the system defined by user. The user initialization program is called in the following format from the initial task.

```
INT userinit( INT flag )

flag = 0 call at initialization
      = -1 call at termination

Return code: 1          Startup usermain()
              Others    terminate the system
```

This program is called with flag=0 at a system initialization, and called with flag=1 at a system termination. Return code is ignored in calling at a system termination. Following is a processing flow.

```
fin = userinit(0);
if ( fin > 0 ) usermain(){};
userinit(-1);
```

The user initialization program is executed in the context of initial task. The task priority is (CFN_MAX_PRI-2).

5 micro T-Kernel Implementation Definitions

5.1 System State Detection

(1) Task independent part (interrupt handler or timer event handler)

Detection is made based on a software flag "knl_taskindp" set in micro T-Kernel.

```
knl_taskindp      = 0    Task portion
knl_taskindp      > 0    Task-independent portion
```

(2) Quasi-task part (extended SVC handler)

Detection is made based on the software flag set in micro T-Kernel.

```
sysmode of TCB = 0    Task portion
sysmode of TCB > 0    Quasi-task portion
```

5.2 Exception/Interrupt used in micro T-Kernel

The following software interrupts are used in micro T-Kernel.

```
INT #32          micro T-Kernel system calls/extended SVC call
INT #34          "tk_ret_int()" system call
INT #35          Task dispatcher call
INT #39          Debugger support function

#21             Timer A0 interrupt
```

5.3 System Call/Extended SVC Interface

The caller side can select either the method of calling interface library in C language function call format or calling directly in C language function call format. (Selectable by configuration file when building Kernel)

Ordinarily the same functional format using interface libraries as above is applied to calling from an assembler. It is also possible to directly call using an INT instruction by the processing equivalent to that of an interface library. Even in this case, the register-saving rules must conform to the NC30 C language rules.

The basic processing of an interface library is executed as below:

- The function code is set in the R3 register and system call is invoked by "INT #32". A function code negative value indicates a system call while the one at 0 or a positive value indicates an extended SVC. However, note that "INT #39" is used for Debugger Support service calls.
- Registers are saved as follows in accordance with NC30 C language register-saving rule:

```
Frame pointer:      FP
Stack pointer:      SP
Arguments (<= 16-bit):  R1, R2
Return value:       R0
```

Other registers are saved.

(1) System call interface

The system call interfaces slightly differ depending on whether there is any 32-bit type argument in the first two arguments. There are two cases here:

Case (1) System call without 32-bit type arguments

Parameters of up to second are set to registers, and the ones of third or more are saved onto the stack. A system call is invoked by "INT #32" ("INT #TRAP_SVC").

An example of system call interface implementation is shown as follows.

```
ER tk_xxx_yyy(p1, p2, p3, p4, p5)

//                               stack state
// High Address +-----+
//              | p5 (16-bit) |
//              | p4 (16-bit) |
//              | p3 (16-bit) |
//              +-----+
//              |   (4bit)   | saved by I/F call
//              | PC (20bit) |
// Low Address  +-----+
//
//              R1 = p1
//              R2 = p2
// Function code is set in R3

$tk_xxx_yyy:
    mov.w    #TFN_XXX_YYY, r3
.if USE_TRAP == 1
    int     #TRAP_SVC
.else
    .glb    _knl_call_entry
    jsr    _knl_call_entry
.endif
    rts
```

Case (2) System call with 32-bit type arguments

Parameters (16-bit) of up to second are set to registers, and the ones of third or more are saved onto the stack. Note, all the 32-bit parameters are saved onto the stack. A system call is invoked by "INT #32" ("INT #TRAP_SVC").

An example of system call interface implementation is shown as follows.

```

ER tk_xxx_yyy(p1, p2, p3, p4, p5)

$tk_xxx_yyy:
    mov.w    #(TFN_XXX_YYY + n), r3
.if USE_TRAP == 1
    int     #TRAP_SVC
.else
    .glb    _knl_call_entry
    jsr     _knl_call_entry
.endif
    rts

```

In this case, the function code is increased by n , which depends on the number of 32-bit type arguments and whether the first or second argument is 32-bit type.

If $p1$ is 16-bit and $p2$ is 16-bit, $n = (\text{number of 32-bit type arguments})$

If $p1$ is 32-bit and $p2$ is 16-bit, $n = (\text{number of 32-bit type arguments}) + 1$

If $p1$ is 16-bit and $p2$ is 32-bit, $n = (\text{number of 32-bit type arguments}) + 1$

If $p1$ is 32-bit and $p2$ is 32-bit, $n = (\text{number of 32-bit type arguments}) + 2$

If $p1$ is the only argument and is 32-bit type, $n = 3$

(2) Extended SVC interface

Regarding an extended SVC, arguments are wrapped in a packet by the caller, and the start address of packet is set in R1 register. An extended SVC call is invoked by `INT #32 ("TRAPA #TRAP_SVC")`.

Normally, the packet is created in a stack area, but can be used in other areas as well. There are no restrictions on the number or types of arguments since argument is wrapped into packet.

An example of extended SVC interface implementation is shown below.

The following additional assumptions are imposed for micro T-Kernel on M16C:

- *All arguments are stored on stack by NC30 compiler.*
- *No function prototype is defined for the extended SVC.*

```

W zxxx_yyy(p1, p2, p3, p4, p5, p6)

/*
 * The function prototype should be removed
 * W zxxx_yyy(p1, p2, p3, p4, p5, p6);
 */

_zxxx_yyy:
    ; All argument value has been stored into stack by compiler
    mov.w    #TFN_XXX_YYY, r3    ; R3 = Function code
    stc     sp, r1                ; R1 = Argument packet top address
    add.w   #03, r1
.if USE_TRAP == 1
    int     #TRAP_SVC
.else
    .glb    _knl_call_entry
    jsr     _knl_call_entry
.endif
    rts

```

(3) micro T-Kernel Debugger Support system call interface

Debugger Support Functions system calls are essentially like other micro T-Kernel service calls, but are called using "INT #32" ("INT #TRAP_DEBUG").

An example of Debugger Support system call interface implementation is shown as follows.

```
ER td_xxx_yyy(p1, p2, p3, p4, p5)

$td_xxx_yyy:
    mov.w    #TDFN_XXX_YYY, r3
    .if USE_TRAP == 1
        int    #TRAP_DEBUG
    .else
        .glb    _knl_call_dbgsp
        jsr    _knl_call_dbgsp
    .endif
    rts
```

5.4 Interrupt Handlers

The interrupt handler hardware-dependent implementation definitions are indicated below.

- Interrupt handler definition information T_DINT

```
typedef struct t_dint {
    ATR intatr; /* Interrupt handler attribute */
    FP inthdr; /* Interrupt handler address */
}T_DINT;
```

- Interrupt definition numbers: "dintno"
Definition numbers "dintno" can be designated in the range from 0 to 63.

The interrupt handler differs in accordance with attribute ("TA_HLNG" or "TA_ASM").

(1) "TA_HLNG" attribute interrupt handlers

When the interrupt handler attribute is "TA_HLNG", the address of the high level programming language support routine within micro T-Kernel is set to interrupt vector table. And, the interrupt handler specified by the high level programming language support routine is called.

The definitions of interrupt handlers are as below.

```
void inthdr ( UINT dintno );
```

- dintno: vector number of the raised interrupt.

At the high level language support routine in micro T-Kernel, no processing is executed to the interrupt controller. Operations such as clearing of the interrupt shall be processed by the interrupt handler.

The restoring from interrupt handler is executed by "return" ("rts" instruction) from function.

(2) "TA_ASM" attribute interrupt handlers

When the attribute of interrupt handler is "TA_ASM", the address of the interrupt handler is directly set to the interrupt vector table.

Since this handler does not go through a high-level language support routine, the flag used to detect a task-independent portion is not updated. Therefore, it is not detected as a task-dependent portion, and the following shall be noted.

If interrupts are enabled (FLG.I = 1 and FLG.IPL < KNL_INTMASK), a task dispatch may occur. Other than the exceptions caused by an INT instruction, the processing after the occurrence of task dispatch will be abnormal. It is therefore necessary to set the task-independent portion detection flag as needed when enabling interrupts in a handler.

The task-independent portion flag is set by operating the "knl_taskindp" flag in system common information. This operation shall be executed in an interrupt disable state (FLG.I = 1 and FLG.IPL >= KNL_INTMASK). In addition, flag shall be certainly returned before exiting the interrupt handler.

```
knl_taskindp ++; /* enter task-independent portion */
knl_taskindp --; /* exit task-independent portion */
```

Because of nested interrupt and the like, "knl_taskindp" shall always be set by increment and decrement. A direct value such as "knl_taskindp = 0" shall never be set.

For returning from an interrupt handler, the "tk_ret_int()" system call is used.

Unlike other system calls, the "tk_ret_int()" system call is invoked using the dedicated software interrupt INT #34. It cannot be called in functional form, unlike other system calls.

```
INT #34      ; Call"tk_ret_int()" (non-return)
```

Before calling "tk_ret_int()", the stack must be put in the state as shown below, and all registers other than the ones saved in the stack (R0 to R3, SB, FB) shall be restored.

High Address	+-----+	
	FLGH(4) PCH(4)	Saved when interrupt occur
	FLGL(8bit)	
	PCM(8bit)	
	PCL(8bit)	
	A1(16bit)	
	A0(16bit)	
	+-----+	
	FLGH(4) PCH(4)	Save by 'int TRAP_RETINT'
	FLGL(8bit)	
	PCM(8bit)	
SP ->	PCL(8bit)	
Low Address	+-----+	

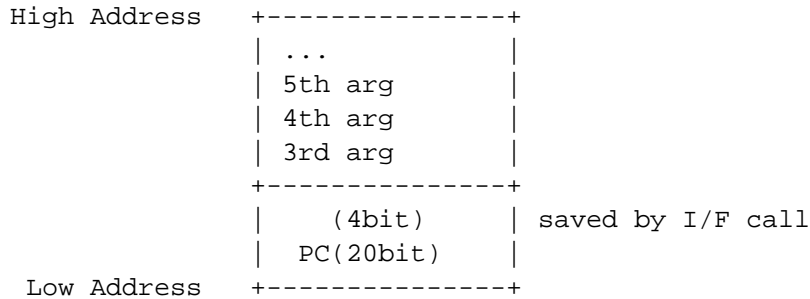
The operation is not guaranteed if "tk_ret_int()" is called by non-interrupt handlers.

5.5 Timer Event Handlers

Timer event handler is called via high-level language support routine even if "TA_ASM" attribute is specified to a handler attribute (just like "TA_HLNG" attribute). Thus, when "TA_ASM" attribute is designated, the parameter("exinf") to be passed to handler is passed to R1 register in accordance with NC30 C language rules. Besides, the register shall be saved in accordance with NC30 C language rules.

5.6 Stack when system call is invoked

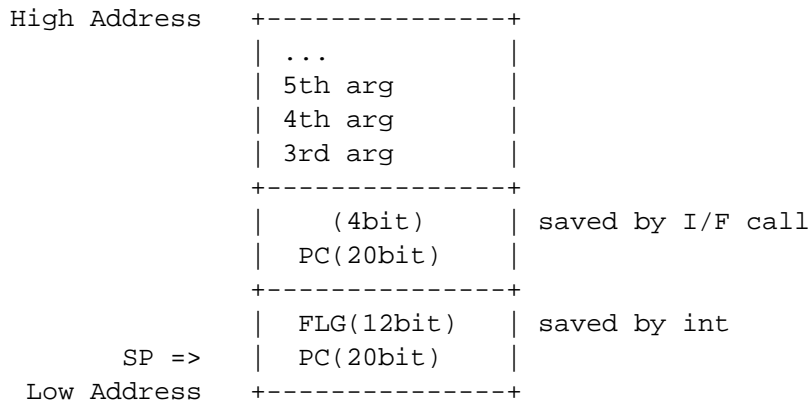
(1) C language I/F (func(arg1, and arg2, ...))



R1 = 1st arg (16-bit)
 R2 = 2st arg (16-bit)

Note: the above example is the only for the case that the first two arguments are all 16-bit data. However, if the argument (either 1st or 2nd argument) is a 32-bit data, it will be pushed onto the stack before the 3rd argument in accordance with NC30 C language rules.

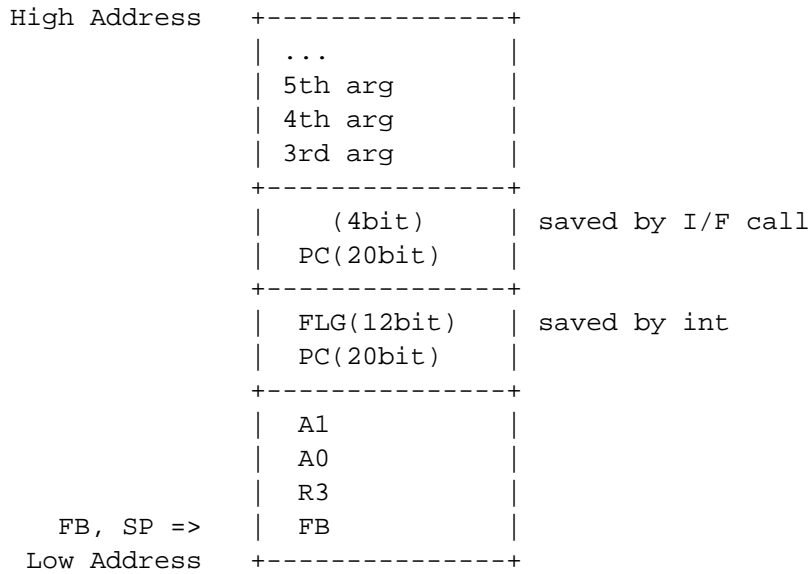
(2) "knl_call_entry" top (immediately after "int #TRAP_SVC" is executed)



R1 = 1st arg (16-bit)
 R2 = 2st arg (16-bit)
 R3 = fncd

Note: the above example is the only for the case that the first two arguments are all 16-bit data. However, if the argument (either 1st or 2nd argument) is a 32-bit data, it will be pushed onto the stack before the 3rd argument in accordance with NC30 C language rules.

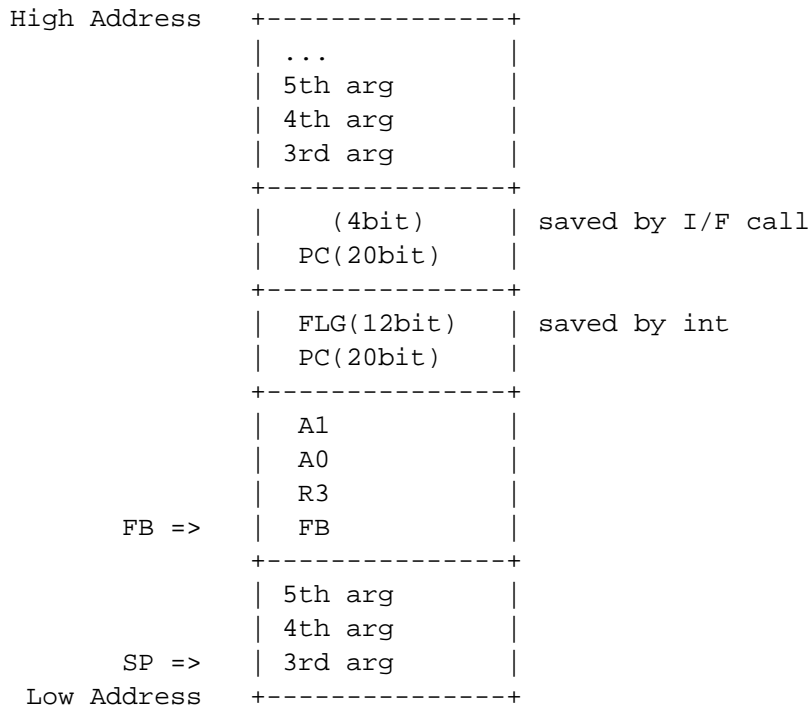
(3) Immediately after "jnz l_esvc_function" is executed



R0 = fncd
 R1 = 1st arg (16-bit)
 R2 = 2st arg (16-bit)
 R3 = fncd

Note: the above example is the only for the case that the first two arguments are all 16-bit data. However, if the argument (either 1st or 2nd argument) is a 32-bit data, it will be pushed onto the stack before the 3rd argument in accordance with NC30 C language rules.

(4) Immediately before system call is invoked

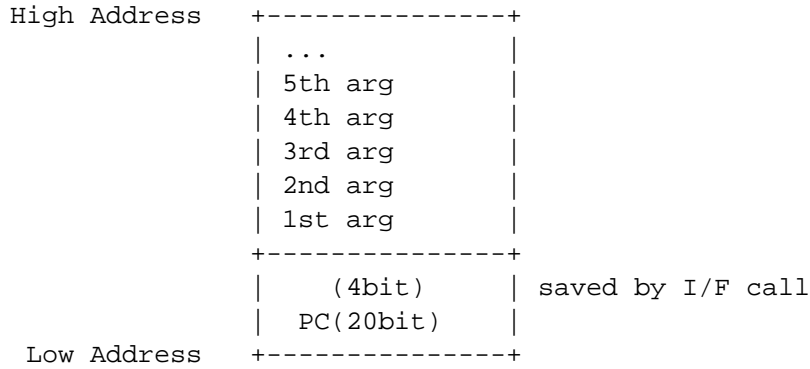


R1 = 1st arg (16-bit)
 R2 = 2st arg (16-bit)

Note: the above example is the only for the case that the first two arguments are all 16-bit data. However, if the argument (either 1st or 2nd argument) is a 32-bit data, it will be pushed onto the stack before the 3rd argument in accordance with NC30 C language rules.

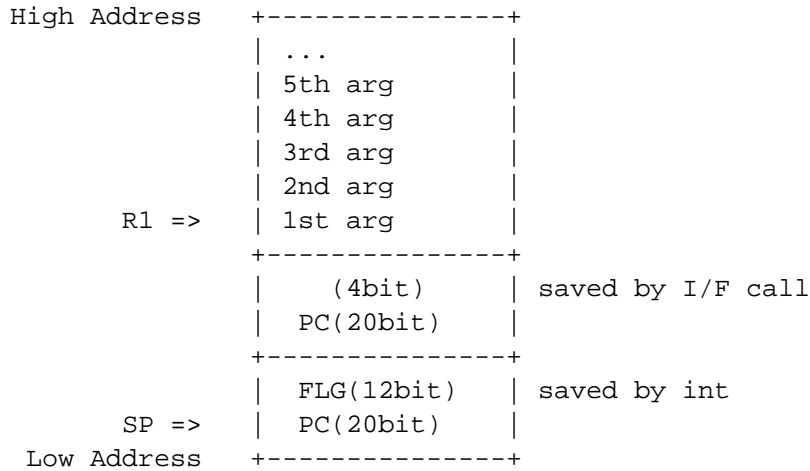
5.7 Stack when the extended SVC is invoked

(1) C language I/F (func(arg1, and arg2, ...))



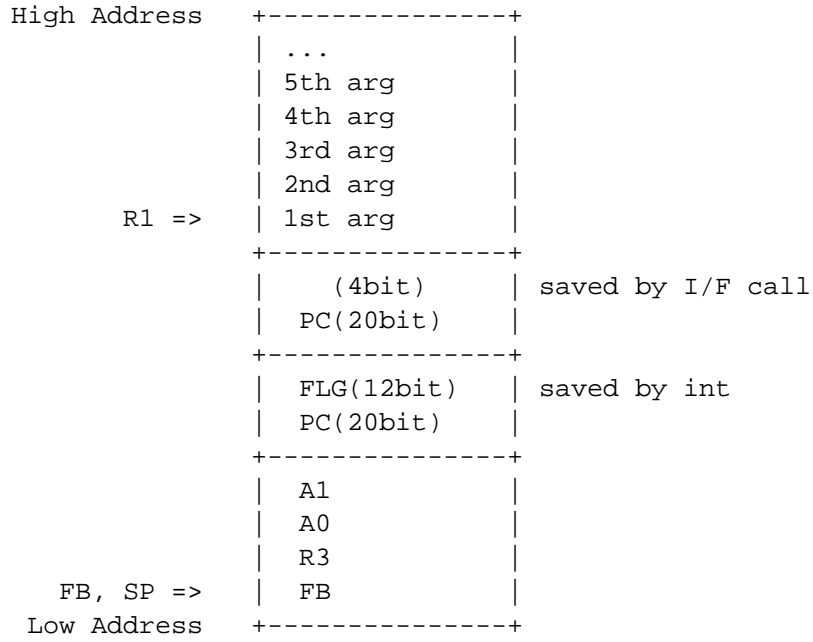
Note: for extended SVC call implementation on micro T-Kernel on M16C, we impose an restriction that no function prototype is defined for extended SVC. Thus, all the arguments are stored on stack in accordance with NC30 C language rules.

(2) "knl_call_entry" top (immediately after "int #TRAP_SVC" is executed)



R1 = pk_para
R3 = fncd

(3) "l_esvc_function" top

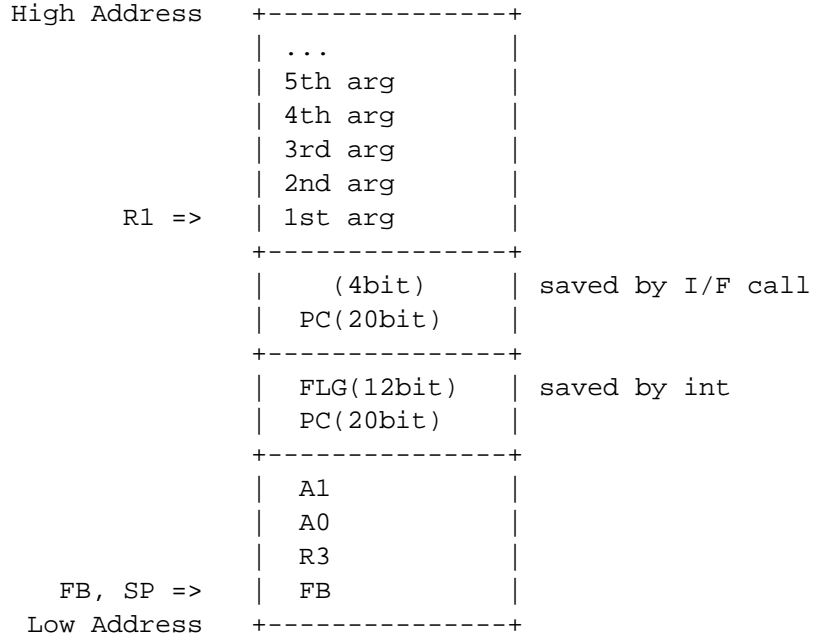


saved by I/F call

saved by int

R0 = fncd
 R1 = pk_para
 R3 = fncd

(4) "knl_svc_ientry" top



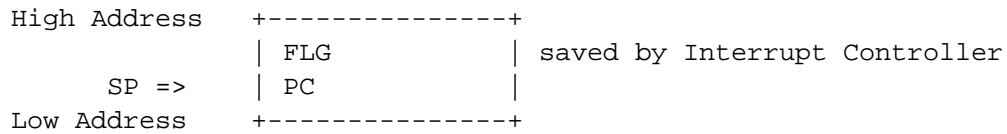
saved by I/F call

saved by int

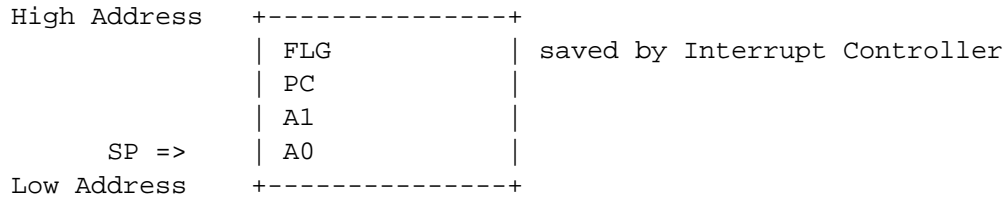
R0 = fncd
 R1 = pk_para
 R2 = fncd
 R3 = fncd

5.8 Stack when an interrupt is raised

- Stack when hardware interrupt is raised



- Stack of the interrupt handler entry when `tk_def_int()` is used.
(called from `kn1_inthdr_entryN` (N is an interrupt vector number))



A0 = interrupt vector number

5.9 Task implementation-dependent definitions

The definitions of task hardware-dependent implementation are given below.

(1) Task creation information T_CTSK

```
typedef void _far *VFP; /* Nonuniform type far pointer (32-bit addr) */

typedef struct t_ctsk {
    VFP      exinf;          /* Extended information */
    ATR      tskatr;        /* Task attribute */
    FP       task;          /* Task startup address */
    PRI      itskpri;       /* Priority at task startup */
    W        stksz;         /* User stack size (byte) */
    UB       dsname[8];     /* Object name */
    VP       bufptr;        /* User buffer */
} T_CTSK;
```

(2) Task attributes

There is no implementation-dependent information.

```
tskatr := (TA_ASM || TA_HLNG)
         | [TA_USERBUF] | [TA_DSNAME]
         | (TA_RNG0 || TA_RNG1 || TA_RNG2 || TA_RNG3)
```

(3) Task format

The format of task is as follows, and makes no difference even if either `TA_HLNG` or `TA_ASM` is specified.

```
void task( INT stacd, VP exinf );
```

The register states when a task is started are as follows.

```

    FLG.I = 1    Interrupts enabled
    R1 = stacd  Task startup parameter
    R2 = exinf  Task extension information
    SP                stack pointer

```

Note: if exinf is a near pointer (16-bit), only the lower 16-bit data will be used.

The other register values are indeterminate.

The "tk_ext_tsk()" or "tk_exd_tsk()" shall be used to exit task. Task doesn't exit by a simple return. The behavior is not guaranteed when the "return" is executed.

5.10 Task registers

```

ER tk_set_reg( ID tskid, T_REGS *pk_regs, T_EIT *pk_eit, T_CREGS *pk_cregs )
ER tk_get_reg( ID tskid, T_REGS *pk_regs, T_EIT *pk_eit, T_CREGS *pk_cregs )

```

The target registers for getting/setting of task register ("tk_get/reg()" / "tk_set_reg()") are defined as below.

(1) General-purpose registers T_REGS

```

typedef struct t_regs {
    VW    r[4]; /* Data register R0-R3 */
    VW    a[2]; /* Address register A0-A1 */
    VW    fb;   /* frame base register */
    VW    sb;   /* static base register */
} T_REGS;

```

When setting register to a task in DORMANT state, the task start parameters and the extended information are set in R1 and R2 by "tk_sta_tsk()", and values set by "tk_set_reg()" are therefore discarded.

(2) Registers saved when an exception is raised T_EIT

```

typedef struct t_eit {
    VFP    pc; /* Program counter */
    VW     flg; /* Flag register */
} T_EIT;

```

(3) Control registers T_CREGS

```

typedef struct t_cregs {
    VP     ssp; /* System stack pointer */
} T_CREGS;

```

5.11 System Call/Extended SVC hook routine

The implementation-dependent definitions of system call/extended SVC hook routine are shown below.

(1) Hook routine definition information TD_CALINF

```
typedef struct td_calinf {
    VP      isp;          /* System stack pointer */
} TD_CALINF;
```

System stack at entering into hook routine is as follows.

For USE_TRAP == 1

High Address	+-----+	
		FLG(12bit) saved by int
		PC(20bit)
	+-----+	
		A1
	+-----+	
		A0
	+-----+	
		R3
	+-----+	
ISP/SSP =>		FB
Low Address	+-----+	

For USE_TRAP == 0

High Address	+-----+	
		(4bit) saved by jsr
		PC(20bit)
	+-----+	
		A1
	+-----+	
		A0
	+-----+	
		R3
	+-----+	
ISP/SSP =>		FB
Low Address	+-----+	

(2) Function prototype for "enter()"

There is a restriction for declaring function prototype for "enter()".

The function format for "enter()" has to be defined with variable-length arguments ("...") as follows.

```
VP enter( FN fncd, TD_CALINF *calinf, ... )
```

If "enter()" does not follow the above format, the arguments may not be correctly passed.

5.12 Other implementation-dependent definitions

(1) Task Control Block (TCB) size and allocation

TCBs for all tasks are allocated in `uTK_NI_BSS` section.

The actual size of each TCB depends on the kernel configuration.

The maximum size of TCB is 92 bytes.

(2) Changes to Application Interface for device management functions

In the application interfaces of the following functions, the data type "w" is changed to "INT" in order to adapt to 16-bit microcomputer architecture.

```
ID tk_rea_dev_impl( ID dd, INT start, VP buf, INT size, TMO tmout )
ER tk_srea_dev_impl( ID dd, INT start, VP buf, INT size, INT *asize )
ID tk_wri_dev_impl( ID dd, INT start, VP buf, INT size, TMO tmout )
ER tk_swri_dev_impl( ID dd, INT start, VP buf, INT size, INT *asize )
ID tk_wai_dev_impl( ID dd, ID reqid, INT *asize, ER *ioer, TMO tmout )
```

6 System Configuration Data

utk_config_depend.h defines the setting such as micro T-Kernel System Configuration, the number of resources in micro T-Kernel, and the number of limitation values in micro T-Kernel.

Note that the maximum value of the setting range for each item is a logical maximum value, and that in reality there are limits imposed by the memory usage.

6.1 Setting value of utk_config_depend.h

```
/*
 *   utk_config_depend.h (m16c62p)
 *   System Configuration Definition
 */
```

```
/* ROMINFO */
#define SYSTEMAREA_TOP 0x0400      /* RAM system area top */
#define SYSTEMAREA_END 0x7f00     /* RAM system area end */
```

Area dynamically managed by micro T-Kernel memory management function.
Specify the top address and end address in RAM.

```
/* User definition */
#define RI_USERINIT      NULL /* User initialization program */
```

User definition initialization/termination program.

```
/* Stacks */
#define RI_INTSTACK      0x8000    /* Interrupt stack top(internal RAM end)
 */
#pragma ASM
__RI_INTSTACK__ .define 08000H
#pragma ENDASM
```

Initial position of an interrupt stack.
(The additional macro "__RI_INTSTACK__" is defined for assembly program)

```
/* SYSCONF */
#define CFN_TIMER_PERIOD 10
```

Designate the system timer interrupt cycle (in millisecond).
This is the smallest resolution (accuracy).

```
#define CFN_MAX_TSKID      32
#define CFN_MAX_SEMID     16
#define CFN_MAX_FLGID     16
#define CFN_MAX_MBXID     8
#define CFN_MAX_MTXID     2
#define CFN_MAX_MBFID     8
#define CFN_MAX_PORID     4
#define CFN_MAX_MPLID     2
#define CFN_MAX_MPFID     8
#define CFN_MAX_CYCID     4
#define CFN_MAX_ALMID     8
#define CFN_MAX_SSYID     4
```

Designate the maximum number for each micro T-Kernel object.

Also in the designation for an upper limit, the number of objects actually used by the system must be taken into account.

```
#define CFN_MAX_REGDEV      8
```

Designate the number of the maximum devices that can be registered with "tk_def_dev()".

This sets the limit for the maximum number of physical devices.

```
#define CFN_MAX_OPNDEV     16
```

Designate the maximum number of times "tk_opn_dev()" can be called to open a device.

This sets the limit for the maximum number of device opens.

The setting range is from 1 to the number of the maximum device registrations.

```
#define CFN_MAX_REQDEV     16
```

Designate the maximum number of requests by "tk_rea_dev()", "tk_wri_dev()", "tk_srea_dev()" and "tk_swri_dev()".

This sets the maximum number of request IDs.

```
#define CFN_VER_MAKER      0
#define CFN_VER_PRID      0
#define CFN_VER_SPVER     0x6100
#define CFN_VER_PRVER     0x0000
#define CFN_VER_PRNO1     0
#define CFN_VER_PRNO2     0
#define CFN_VER_PRNO3     0
#define CFN_VER_PRNO4     0
```

Version information (tk_ref_ver).

```
#define CFN_REALMEMEND    ((VP)0x00007fff)
```

Most significant address of RAM used in micro T-Kernel management area.

```
/*
 * Use non-clear section
 */
#define USE_NOINIT        (1)
```

1 : Among the static variables (BSS alignment), the variables that require no initialization are not cleared to zero in Kernel initialization processing. Since the processing for zero-clear execution is reduced, Kernel start-up time is shortened.

0 : All static variables without initialization value (BSS alignment) shall be cleared to zero.

```
/*
 * Use dynamic memory allocation
 */
#define USE_IMALLOC      (1)
```

1 : The dynamic memory allocation function in Kernel is used.

0 : The dynamic memory allocation function in Kernel is not used. When creating the objects for

Task, Message buffer, Fixed-size Memory Pool, and Variable-size Memory Pool, buffer shall be specified by application with TA_USERBUF attribute.

```

/*
 * Use program trace function (in debugger support)
 */
#define USE_HOOK_TRACE    (0)

    1 : The hook function of debugger support function is used.
        However, the hook function can not be used if USE_DBGSP is 0.
    0 : The hook function of debugger support function is not used.

/*
 * Use clean-up sequence
 */
#define USE_CLEANUP      (1)

    1 : Clean up processing of Kernel shall be executed after the termination of application.
    0 : Clean up processing of Kernel shall not be executed after the termination of application. As
for
    the system that doesn't return from usermain function, the consumption of ROM decreases
    by turning off this flag.

/*
 * Use full interrupt vector
 */
#define USE_FULL_VECTOR  (1)

    1 : Prepare the interrupt initialization processing (save of partial register or setting of an
interrupt
    number)for the all interrupt vectors.
    0 : Prepare the initial processing only for the defined interrupts. The consumption of ROM
    decreases.

/*
 * Use high level programming language support routine
 */
#define USE_HLL_INTHDR   (1)

    1 : High level language support routine is used at interruption
    0 : High level language support routine is not used at interruption.
        Jump table, and so on is not used, and the consumption of ROM/RAM decreases.

/*
 * Use dynamic interrupt handler change
 *   USE_FULL_VECTOR must be set to 0 if this macro is set to 0.
 */
#define USE_DYNAMIC_INTHDR    (1)

    1 : Change an interrupt handler with tk_def_int.
    0 : Do not change an interrupt handler with tk_def_int.
        Jump table is not used, and the consumption of ROM is decreased.

```

6.2 Building flag

The following modes are configured in the "Renesas M16C Standard Toolchain" configuration window.

- **USE_TRAP** (trap mode to be configured for “Defines” in C and Assembly Tabs)
 - 1 : use software interrupt (`int`) for system calls
 - 0 : not use software interrupt (`int`)
- **USE_NC30LIB** (to be configured for “Defines” in C Tab Only)
 - 1 : use built-in standard C library from NC30 tool chain
 - 0 : use string library (`libstr`) inside micro T-Kernel
(in this case, the `libstr` needs to be manually inserted during linking phase)

7 Resource Used by micro T-Kernel

7.3 Kernel Objects

The following kernel objects are used in micro T-Kernel system. Meanwhile, ID number is dynamically allocated, and the ID number at micro T-Kernel normal start-up is written.

Type	ID	Name	Description
Task	1	inittsk	Initial task
Message Buffer	-	DEvt	Event notification (Unused by default) Change available in the CFN_DEVT_MBFSZ of utk_config_depend.h
Semaphore	-	(NULL)	Device management synchronous control (Use one semaphore every time device is opened)
Event flag	1	DMLk	Overall device management lock control

Table 7-1 Kernel objects used in micro T-Kernel

micro T-Kernel

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Version: 1.01.00

Released by T-Engine Forum(<http://www.t-engine.org>) at 2008/03/05.

Modified Source Code:

- Renesas Starter Kit for M16C/62P Version m16c62p.1B
- Renesas Starter Kit for M32C/87

Changes:

- Adapted to the Renesas Starter Kit for M16C/62P
- Corresponded to the RENESAS Compiler (M3T-NC30WA).

Changed by RENESAS TECHNOLOGY CORP.
and RENESAS SOLUTIONS CORP.
and RENESAS TECHNOLOGY SINGAPORE PTE. LTD. at 2008/03/10.

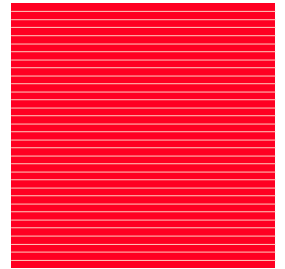
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Renesas Technology Corp.

2-6-2, Ote-machi, Chiyoda-ku, Tokyo, 100-0004, Japan